

Ingegneria Informatica e dei Sistemi – Computer and Control Engineering

Final exam – XXVIII Cycle

Schedule

May 16, 2016 – Sala Riunioni 4, DAUIN

09:00 *Committee setup and roll call*

09:30 ACED LOPEZ Sebastian

10:00 BETTASSA COPET Piergiuseppe

10:30 CANAVESE Daniele

11:00 CERRATO Ivano

11:30 DU Boyang

12:00 PITSCHIEDER Christian

12:30 *Lunch break*

13:30 LOIACONO Carmelo

14:00 TENACE Valerio

14:30 URGESE Gianvito

15:00 VENDRAMINETTO Daniele

15:30 VIRGILIO Matteo

16:00 XHEMBULLA Jetmir

16:30 XIAO Xin

17:00 *Final works of the committee*

All candidates are required to be present at the roll call with a valid ID, at 9:00.

The scheduled times for the presentations are tentative, please be present and ready to start at least 15 minutes before your scheduled time.

The presentations are public.

Everyone (candidates, advisors, other Ph.D. students or other interested persons) are welcome to assist.